

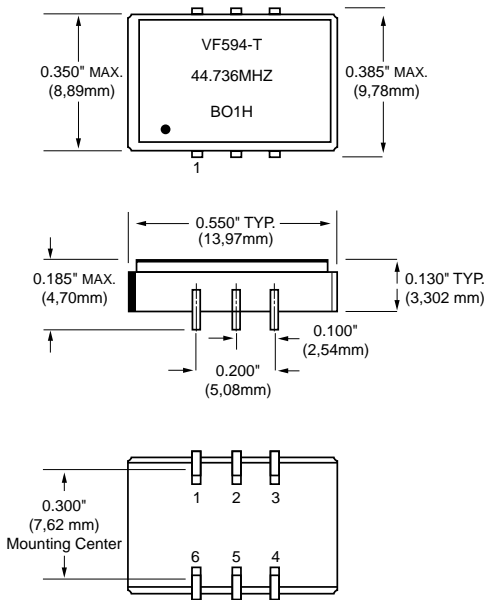
VF594-T



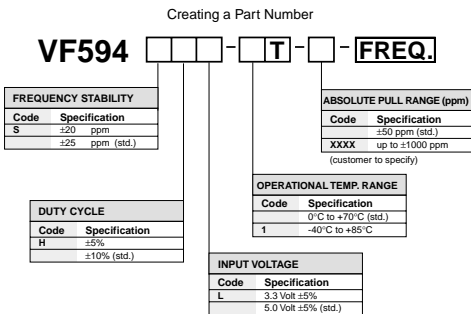
HCMOS/TTL Compatible Tristate VCXO Surface Mount Ceramic Package

FEATURES

- Wide Frequency Range
- Very Low Phase Jitter at All Frequencies
- Wide Pullability (± 1000 ppm available at some frequencies)
- EMI Shielded
- Standard Footprint



All dimensions are typical unless otherwise specified.



Example: VF594SHL-T-100-44.736MHz: Frequency Stability ± 20 ppm, Duty Cycle $\pm 5\%$, Input Voltage 3.3 Volt $\pm 5\%$, Operating Temperature 0°C to +70°C, Tristate Output, APR ± 100 ppm, Frequency 44.736MHz.

	Parameter	Symb	Condition	Min	Typ	Max	Unit	Note
Absolute Max. Ratings	Input Break Down Voltage	V _{cc}		-0.5		7.0	V	
	Storage Temp.	T _s		-55		+85	°C	
	Control Voltage	V _c		-1		9	V	
Electrical	Frequency Range	F		1.54		160	MHz	
	Frequency Stability	$\Delta F/F$	Vs. Temp., V _{cc}			± 25		ppm
	Input Voltage	V _{cc}		4.75 3.15	5.00 3.30	5.25 3.45	V	Std. LV Opt.
	Input Current	I _{cc}	No Load		12		mA	@20MHz
	Load	10 TTL gates or 50pF MAX AC coupled 50 Ohm termination recommended @ F>54 MHz						
	Duty Cycle		@1.4V	40	50	60	%	1
	Rise/Fall Time	Tr/Tf	20% to 80%			6	ns	2
	Logic "1" Level	V _{oh}	Max Load	0.9V _{cc}				
	Logic "0" Level	V _{ol}	Max Load			0.1V _{cc}		
	Start-up Time	T _s			2	10	ms	
	Phase Jitter		1 σ			1	ps	fj>1KHz
	Modulation BW	f _m	@V _c = 2.5V	10			KHz	@-3db
	Input Impedance		f _m <10KHz	50			KOhm	
	Control Voltage	V _c	V _{cc} = 5V V _{cc} = 3.3V	0.00 0.00	2.50 1.65	5.00 3.30	V	3
	Deviation Slope		Monotonic, Positive		50 75		ppm/V	V _{cc} = 5.0V V _{cc} = 3.3V
Absolute Pull Range	APR			± 50		ppm		
Tristate Function	Input HIGH (>2.5V) or floating: ACTIVE Input LOW (<0.5V): INFINITE IMPEDANCE							
Linearity					± 20	%	4	
Setability (V _c for center freq)	V _{c0}	@25°C, F _{nom.}	2.00 1.20	2.50 1.65	3.00 2.10	V	V _{cc} = 5.0V V _{cc} = 3.3V	
Environmental and Mechanical	Operating Temperature Range	0°C to +70°C (-40°C to +85°C available)						
	Mechanical Shock	Per MIL-STD-202, Method 213, Cond. E						
	Thermal Shock	Per MIL-STD-883, Method 1011, Cond. A						
	Vibration	Per MIL-STD-883, Method 2007, Cond. A						
	Soldering Conditions	260°C, for 10s, Max; 230°C, for 90s, Max.						
Hermetic Seal	Leak rate less than 5 x 10 ⁻⁸ atm.cc/s of helium							
Electrical Connections	Pin Out	Pin #1-Voltage Control Pin #3-Ground, Case Pin #5-N/C		Pin #2-Tristate Control Pin #4-Output Pin #6-V _{cc}				

Notes:

1. Tighter duty cycle available.
2. Frequency dependent. Shorter at higher frequencies.
3. 0V to 5V control voltage available for V_{cc} 3.3V. Nominal Control Voltage is 2.5V and Setability is ± 0.5 V in this case.
4. 10% and 5% available.

All specifications are subject to change without notice.