

## SINEWAVE TCXO/VC-TCXO IN 14 PIN DIP HERMETICALLY SEALED PACKAGE - TCHS Series

## FEATURES

- RoHS Compliant (Pb-Free), Tight Stability over Wide Temperature Range
- Available with Voltage Control for Electric Frequency Adjustment
- Sinewave or Clipped Sinewave Output, Low Phase Noise, Low Power Consumption
  Hermetically Sealed Package, Industry de factor Standard Footprint

## **SPECIFICATIONS**

Frequency Range	8 MHz to 40 MHz	
Supply Voltage (Vcc) Input Current Storage Temperature	A = 5.0 VDC ± 5%; B = 3.3 VDC ± 5% 5 mA Maximum -40°C to 85°C	
Controllable Frequency Option Control Voltage (Vc) Setability of Vc at Fnom, 25°C	V = Voltage control: ±5 ppm Minimum 2.5±2.0 VDC for Vcc = 5 VDC; 1.65±1.5 VDC for Vcc = 3.3 VDC 2.5±0.5 V DC for 5.0V part; 1.65±0.4 VDC for 3.3V part	
Frequency Stability vs Temp. Temperature Range Standard Stability	010 = ±1 ppm; 015 = ±1.5 ppm; 020 = ±2 ppm; 025 = ±2.5 ppm; 050 = ±5 ppm A = 0°C to 70°C; B = -40°C to 85°C; F = 0°C to 50°C; H = -30°C to 75°C 025H = ±2.5 ppm / -30°C to 75°C	
Frequency Stability vs Vcc Frequency Stability vs Load Aging Phase Noise Non-harmonic Spurious Harmonic Distortion Output Load Output Waveform Output Level	±0.3 ppm Maximum / Vcc ± 5% ±0.3 ppm Maximum / 10 kOhms// 10 pF ± 10% ±1 ppm Maximum per year @25°C -145 dBc/Hz at 1KHz for 10MHz -50 dBc Max for 10MHz -20 dBc Max for 10MHz 50 Ohms for pure Sinewave; 10 kOhms/10 pF for clipped Sinewave 1 = Pure Sinewave; 2 = Clipped Sinewave 1.0Vp-p Minimum	
Creating a Part Number       TCHS-10M000-A V 010 A 1         Product Series       I = pure sinewave, 2 = clipped sinewave         Operating Temperature Range: A = 0 to 70°C         Frequency       Frequency         B = 3.3V       V         V       020 = ±2 ppm         blank       025 = ±2.5 ppm		
MARKING AREA 20.8 MAX Pin 1	€ C C C C C C C C C C C C C C C C C C C	6.8 MAX
All dimensions are typical unless otherwise s	Pin Connections #1: Vc or N/C #7: Ground #8: Output pecified #14: Vcc	Glasss Standoffs' Available in Gull Wing Configuration Dimensions in Millimeters